

## **Altera's RapidIO IP Core Passes RIOLAB Level 1 and Level 2 Device Interoperability Testing**

*DIL- Qualification Provides Customers a Greater Level of Confidence When Designing Altera's FPGAs into a RapidIO System*

**Ottawa, Canada – April 27, 2009** – RIOLAB™, a division of Fabric Embedded Tools Corporation and the world's only independent RapidIO® interoperability testing facility, today announced Altera's RapidIO MegaCore® Function IP, version 9.0, successfully passed the first two levels of RIOLAB's Device Interoperability Level (DIL) testing. RIOLAB DIL qualified Altera's RapidIO IP core using a Stratix® II GX FPGA.

Altera's RapidIO MegaCore Function is available across Altera's mid-range and high-end device series, including its latest 40-nm portfolio consisting of Stratix IV FPGAs, Arria II FPGAs and HardCopy IV ASICs.

"The qualification of RapidIO IP within an FPGA to DIL-1 or DIL-2 is a key first step towards ensuring short time to market for custom designs. By successfully completing DIL-1 and DIL-2 testing, Altera clearly demonstrates its commitment to provide complete RapidIO solutions," said Jim Parisien, president of Fabric Embedded Tools.

RIOLAB tests, based on the RapidIO Trade Association's "RapidIO Device Interoperability and Specification Compliance Checklists, 1.3 Spec," address the graduated levels of interoperability that align with the increasing complexity of both the RapidIO specification and the needs of silicon vendors and OEMs.

DIL-1 tests verify device support for initialization, enumeration and basic read and write packet transactions. In DIL-1 testing, the device-under-test is tested against the entire RIOLAB hardware library for both request and response level transactions, with an emphasis on the reliability of interaction between devices. DIL-2 testing is the first level that delves deeper into register and packet protocol compliance under a variety of

conditions that are not covered within DIL-1. The prerequisite for DIL-2 testing is proof of successfully passing DIL-1.

"Altera has a long history of supporting RapidIO with our FPGAs and IP offerings and this latest milestone directly supports requests from our customers to have our RapidIO IP core certified by RIOLAB," said Luanne Schirrmeister, Senior Director, Component Product Marketing, Altera Corporation. "Being the first FPGA vendor to obtain RIOLAB DIL-1 and DIL-2 certification provides our customers a greater level of assurance our transceiver-based devices are interoperable within their RapidIO ecosystem."

"Interoperability Reports from RIOLAB are fundamental tools for OEMs to determine how well a RapidIO device and/or IP functions in a real system environment and with multi-vendor switches and endpoints," said Tom Cox, executive director of the RapidIO Trade Association. "This continues to be even more important as our ecosystem is growing to include over a dozen switches and many more endpoints."

### **About Fabric Embedded Tools**

Fabric Embedded Tools Corporation (<http://www.fetcorp.com>) is the leading provider of RapidIO software, network management and diagnostic tools. The company delivers innovative solutions that shorten product development and testing cycles, and reduce technology risks and time-to-market. Through its unwavering commitment to delivering powerful, time-saving tools and services, excellence in customer support, and strong partner relationships, FET meets the needs of semiconductor vendors, single board computer vendors, and OEMs across the embedded industry.

A division of Fabric Embedded Tools, RIOLAB ([www.rio-lab.com](http://www.rio-lab.com)), is a state-of-the-art RapidIO interoperability testing facility that provides device interoperability and specification compliance reports that meet the growing needs of silicon vendors and OEMs designing with RapidIO technology. The lab is the only facility in existence that provides commercial semiconductor vendors, FPGA and ASIC manufacturers with an

unbiased common vehicle for demonstrating device interoperability and specification compliance to the RapidIO standard.

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