



Tundra Semiconductor Creates World's First RapidIO® Interoperability Lab

The RIOLAB™ Addresses Component Manufactures' and OEMs' Need to Accelerate Next-Generation RapidIO Products to Market

Texas Instruments Developers Conference 2006, Dallas, Texas - February 28, 2006 --

Members of the RapidIO Trade Association steering committee including Ericsson, Freescale Semiconductor, Lucent Technologies, Texas Instruments, Xilinx, and others immediately communicated their support for the RIOLAB™ initiative as a critical milestone in furthering deployment of RapidIO-based products.

"It is very encouraging to see the formation of this lab at a time when there is an immediate and expanding need for interoperability testing of products currently on the market as well as new RapidIO-based devices that continue to be introduced by a growing number of semiconductor manufacturers," said Dave Wickliff, distinguished member of technical staff at Lucent Technologies and Chairman of the RapidIO Trade Association's Steering Committee.

"We applaud Tundra's initiative in the launch of the RapidIO Interoperability Lab, a facility that meets the need for interoperability test reports that will assist the whole industry in a faster and more thorough evaluation of RapidIO technology and existing semiconductor devices," said Dr. Peter Olanders, research manager at Ericsson and vice chairman of the RapidIO Trade Association. "This lab will produce a critical piece of the due diligence required to move forward."

"The interoperability lab will provide TI with an additional tool to show customers that our next-generation RapidIO products are the right choice for wireless infrastructure," said Dr. Alan Gatherer, chief technology officer for Texas Instruments Communications Infrastructure Group. "Formalizing a RapidIO Interoperability Lab and ensuring it is operated as an independent entity creates an enormously valuable resource for every company designing RapidIO-based components and systems."

"We look forward to leveraging the capability that the RapidIO Interoperability Lab will provide to facilitate Freescale's efforts to meet our customers' needs," commented Greg Shippen, system architect for Freescale Semiconductor, Inc. "We congratulate Tundra for its leadership in taking this step, and in its commitment to keeping the RapidIO Interoperability Lab independent, a strategy that should provide an extremely high level of confidence among both the RapidIO ecosystem and OEMs."

"With the availability of RapidIO IP cores for Virtex(TM)-4 FX FPGAs, it is even more important for Xilinx to see the realization of a formal RapidIO Interoperability Lab through which we can demonstrate interoperability with a growing number of other end point and switch vendors," said Kourosh Matloubi, senior manager PCIe, RIO, ASI IPs at Xilinx, Inc.

-more-



“Altera welcomes the initiative that Tundra has undertaken to accelerate the formation of a RapidIO Interoperability Lab,” said Steve Mensor, director of IP & Technology Marketing at Altera. “Altera's FPGA's and IP offerings have successfully supported RapidIO over several years and this lab is further evidence of the maturity of RapidIO as a next-generation interconnect technology and comes at a time when customer demand requires the level of certification that can only be delivered by an independent interoperability lab.”

"Tundra's RIOLAB commitment will further strengthen RapidIO interoperability efforts and accelerate the establishment of an independent third-party lab facility to support the RapidIO Trade Association," said Tracy Richardson, director of the Silicon Solutions Group, Advanced Solutions business unit at Mercury. "We look forward to performing vendor interoperability tests between our products and other RapidIO devices."

-30-

TUNDRA is a registered trademark of Tundra Semiconductor Corporation (Canada, U.S. and U.K.). TUNDRA and the Tundra logo are registered marks of Tundra Semiconductor Corporation (Canada - registration in the United States, European Union, and People's Republic of China pending). RIOLAB and the RIOLAB logo are marks of the RapidIO Interoperability Lab. RapidIO is a trademark of the RapidIO Trade Association, Inc. Other registered and unregistered trademarks are the property of their respective owners.

© Copyright 2006 Tundra Semiconductor Corporation. All rights reserved. Information subject to change without notice.

For more information contact:

North America

Phyllis Grabot, + 805.341.7269

Phyllis@corridorcomms.com

Europe

Rob Davies, + 44 (0)1225 470000

rob.davies@publitek.com

China

Romy Arambulo, + 852.2837.4727

Romy.Arambulo@edelman.com